

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTOR(S): Keiichiro KATA et al.  
Title: PROCESS FOR  
MANUFACTURING  
SEMICONDUCTOR DEVICE  
AND SEMICONDUCTOR  
WAFER  
Original Patent Number: USP 5,844,304  
Original Patent Issue Date: December 1, 1998  
Examiner: To be determined  
Art Unit: To be determined



**REISSUE PATENT APPLICATION TRANSMITTAL**

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.173 is an application for reissue of Utility Patent as identified above:

Keiichiro KATA  
c/o NEC Electronics Corporation  
1753 Shimo-Numabe  
Nakahara-ku, Kawasaki-si  
Kanagawa, Japan

-and-

Shinichi CHIKAKI  
c/o NEC Electronics Corporation  
1753 Shimo-Numabe  
Nakahara-ku, Kawasaki-si  
Kanagawa, Japan

In connection with this application, the following documents are enclosed:

- [ X ] Specification, Claims and cover with Abstract, in double column copy of format (6-pages);
- [ X ] Drawing Transmittal with 4 sheets (Figs. 1A-4C (20 total sub-Figures) attached);

- ☒ Reissue Declaration and Power of Attorney Under 37 C.F.R. § 1.175 (6-pages)
- ☐ English Translation Document of Reissue Oath/Declaration (if applicable)
- ☒ Original Patent (37 CFR § 1.178)
- ☒ Written Consent of all Assignees to reissue application
- ☒ 37 CFR §3.73(b) Certificate
- ☐ Statement of status and support for all changes to the claims (37 CFR §1.173(c))
- ☐ Foreign Priority Claim (35 USC 119)
- ☒ Preliminary Amendment and 37 C.F.R. 1.173(c) Statement
- ☒ Information Disclosure Statement with Form PTO-SB08 with copies of 4 listed references;
- ☒ Application Data Sheet (37 CFR 1.76) (3 pages)
- ☒ Return Receipt Postcard (MPEP §503)
- ☐ Applicant Claims Small Entity Status
- ☐ Notice of Litigation and Request for Special Dispatch under 37 C.F.R. § 1.607(b)

**FEE CALCULATIONS:** including Amendments to claims. (Small entity fees indicated in parentheses.)

Reissue Application Fee Form						Docket Number (Optional)			
Claims as Filed – Part I									
Claims In Patent		Number Filed in Reissue Application	(3) Number Extra	Small Entity			Other than a Small Entity		
				Rate	Fee		Rate	Fee	
(A) 12	Total Claims (37 CFR 1.16(j))	(B) 32	12	x \$ _____ = \$ _____		or	0 x \$18.00 =	\$0 \$216.00	
(C) 4	Independent Claims (37 CFR 1.16(j))	(D) 4	2	x \$ _____ = \$ _____			0 x \$80.00 = \$0	\$0	
Basic Fee (37 CFR 1.16(h))							OR		\$750.00
Total Filing Fee									\$750.00



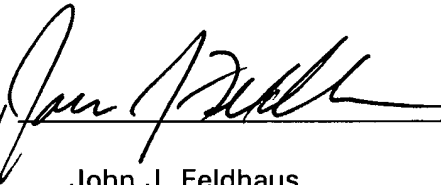
**METHOD OF PAYMENT:**

- [ X ] A check in the amount of \$894.00 to cover the filing fee is enclosed.
- [ ] The required filing fees are not enclosed but will be submitted in response to the Notice to File Missing Parts of Application.
- [ X ] The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date: 8/22/03

By 

FOLEY & LARDNER  
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John J. Feldhaus  
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***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: Keiichiro KATA et al.  
Title: PROCESS FOR  
MANUFACTURING  
SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR WAFER  
Appl. No.: Application for Reissue of US  
Patent No. 5,844,304, granted  
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Examiner: TBD  
Art Unit: TBD

**TRANSMITTAL OF FORMAL DRAWINGS**

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

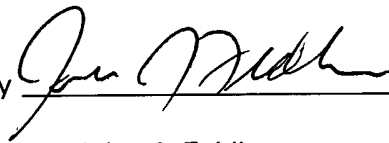
Sir:  
Transmitted herewith are the formal drawings (4 sheets, Figures 1A-1C, 2A-2G, 3A-3G, 4A-4C) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date

8/24/07

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